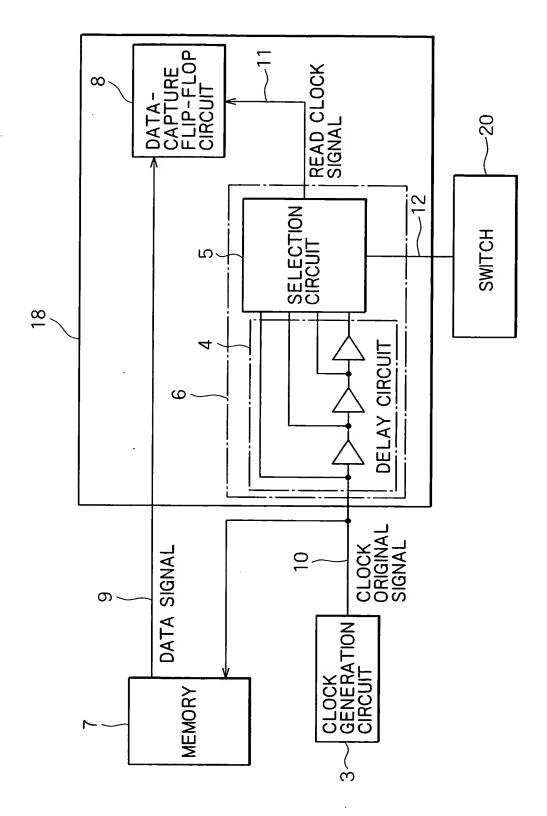
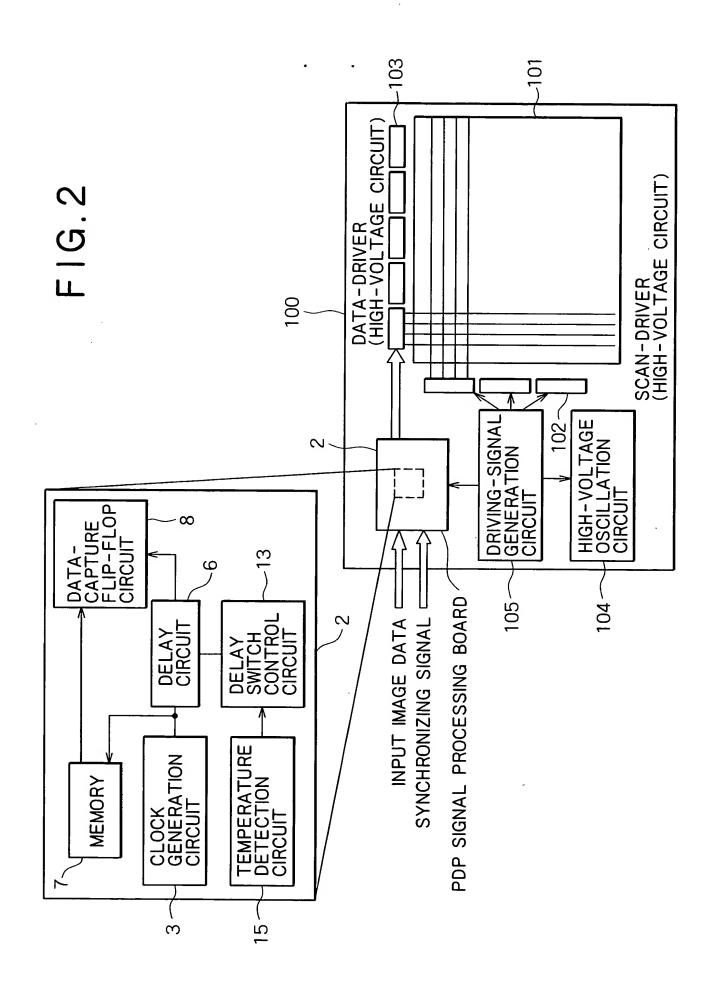
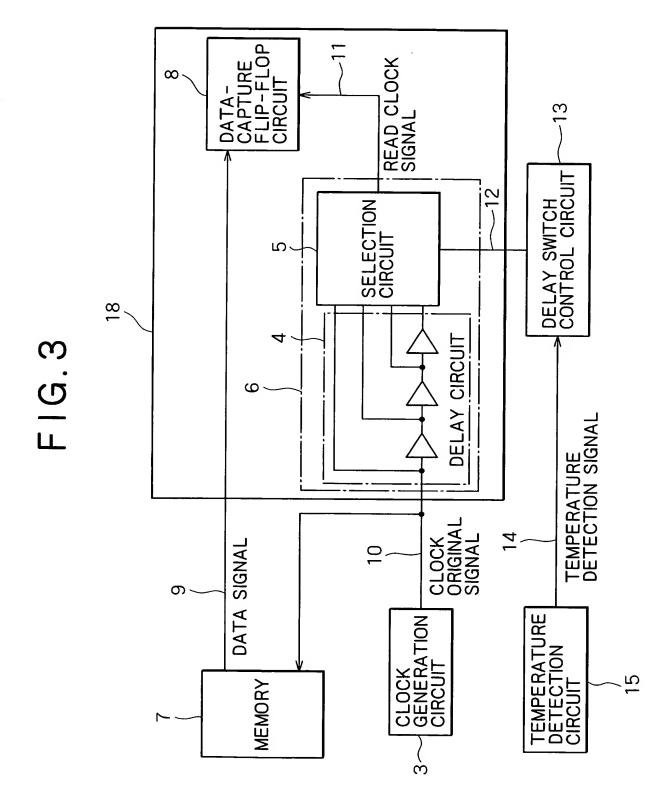
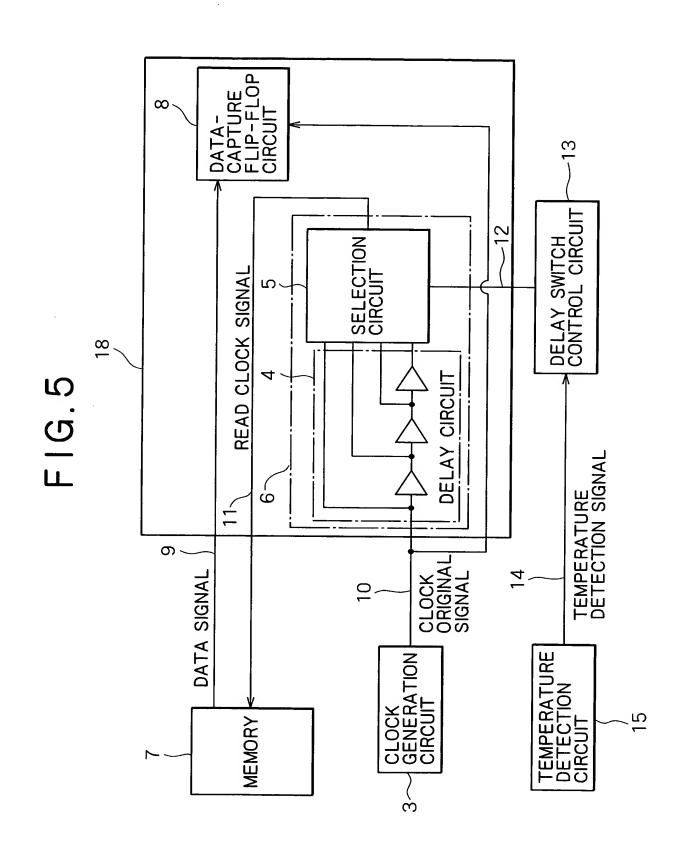
FIG. 1 (PRIOR ART)







DATA-CAPTURE FLIP-FLOP CIRCUIT WRITE CLOCK SIGNAL ∞ DELAY SWITCH CONTROL CIRCUIT -12 SELECTION 2 3 F1G.4 DELAY CIRCUIT 9 TEMPERATUŘE DETECTION SIGNAL CLOCK ORIGINAL SIGNAL 7 DATA SIGNAL 10 တ TEMPERATURE DETECTION CIRCUIT CLOCK GENERATIC CIRCUIT 15 MEMORY 3)



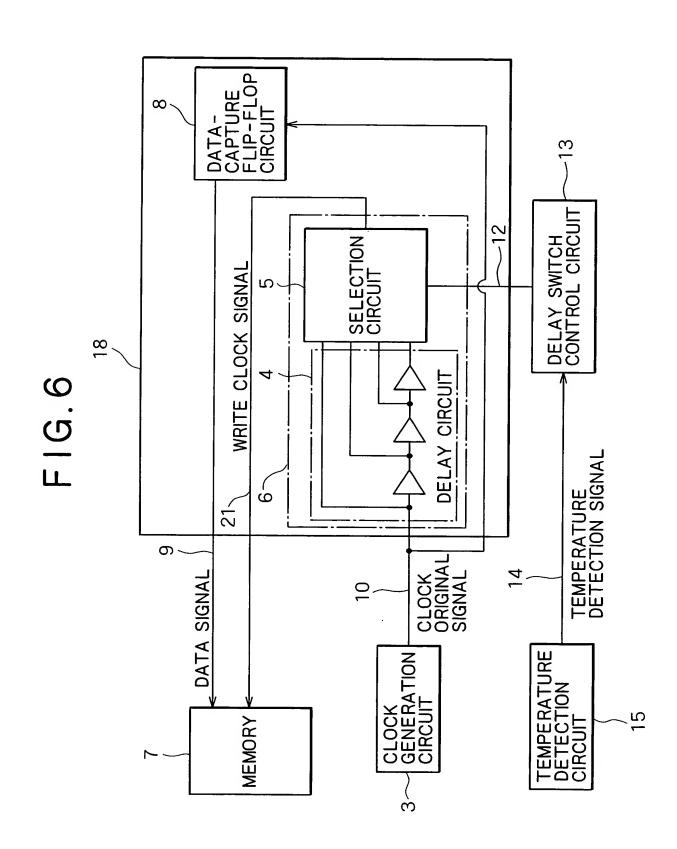
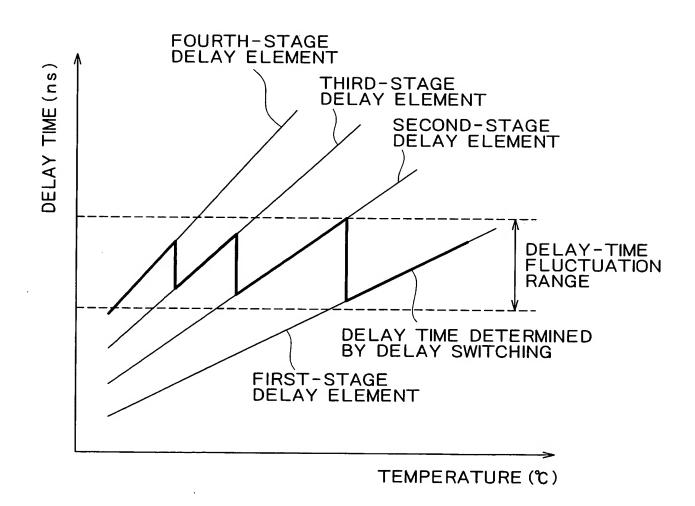
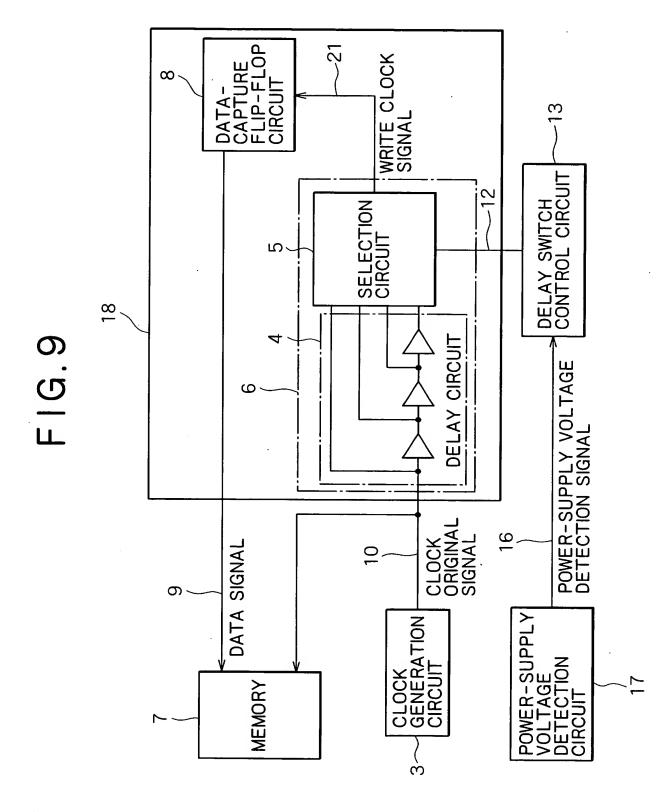
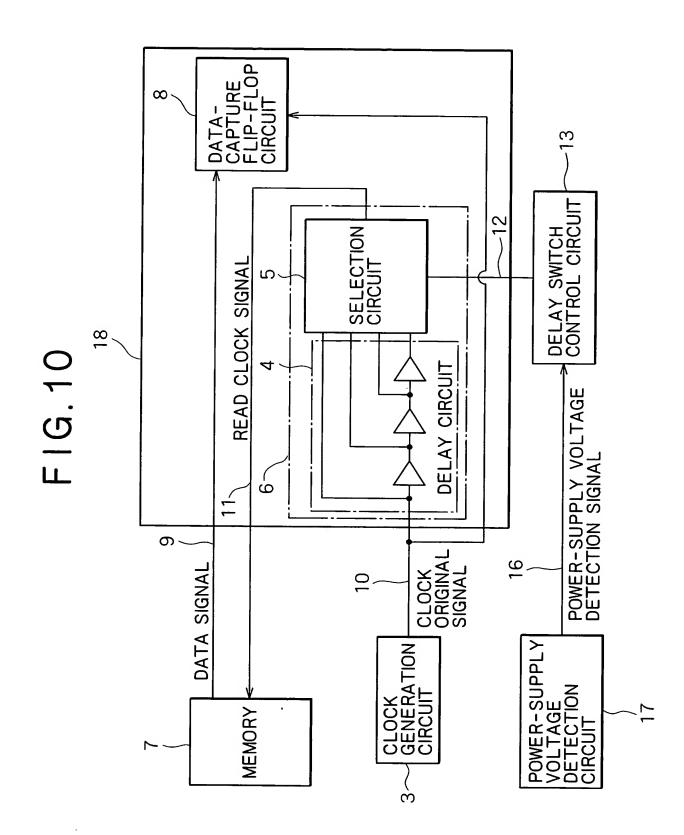


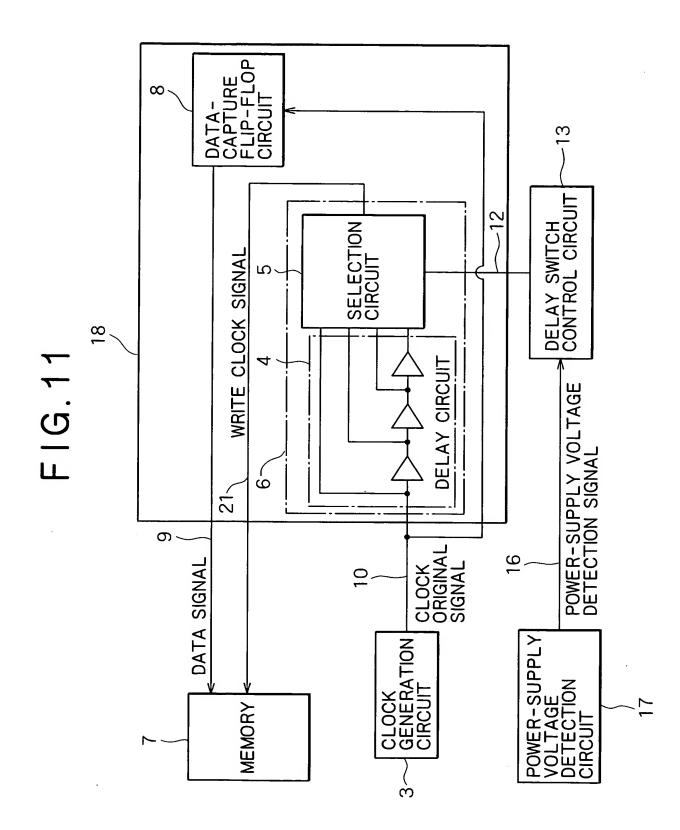
FIG.7

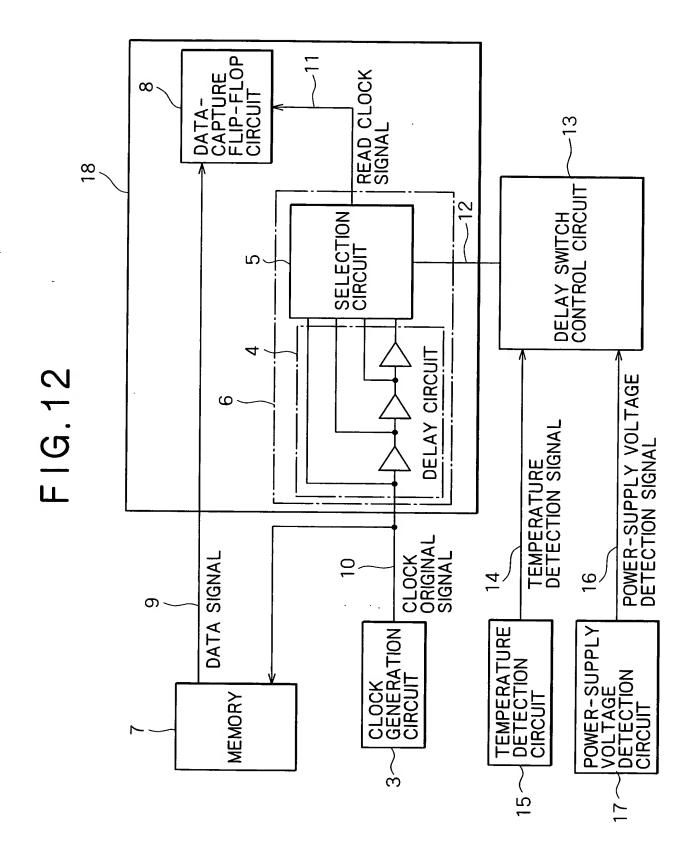


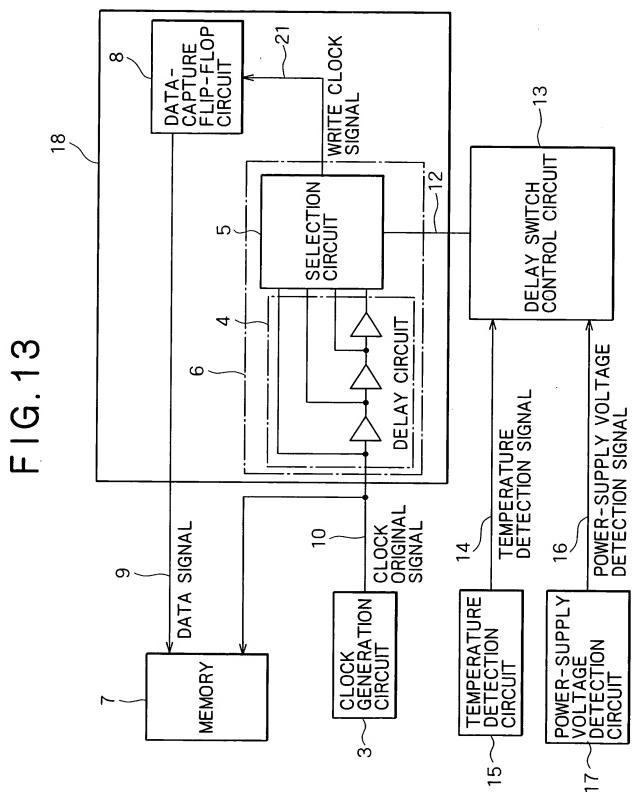
DATA-CAPTURE FLIP-FLOI CIRCUIT READ CLOCK SIGNAL ∞ DELAY SWITCH CONTROL CIRCUIT SELECTION CIRCUIT S <u>∞</u> F 1 G. 8 DELAY CIRCUIT POWER-SUPPLY VOLTAGE DETECTION SIGNAL 9 CLOCK ORIGINAL SIGNAL 16 DATA SIGNAL 6 POWER-SUPPLY VOLTAGE DETECTION CLOCK GENERATION CIRCUIT MEMORY CIRCUIT

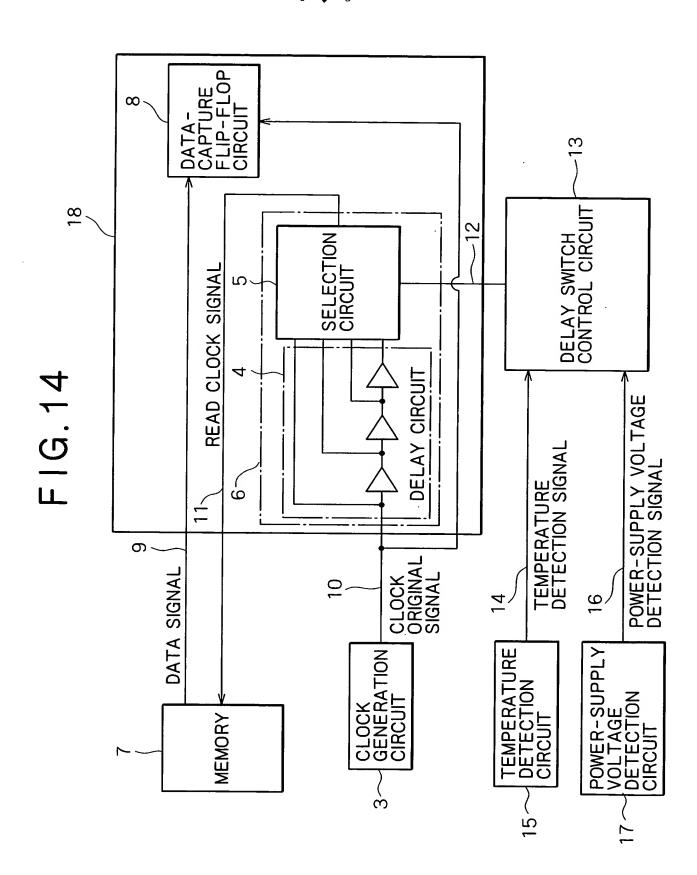


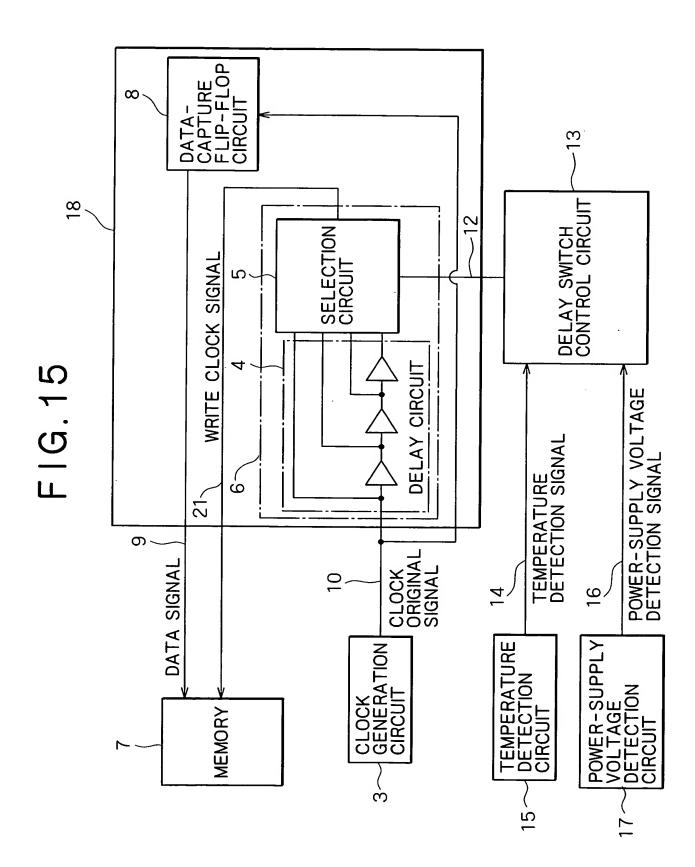


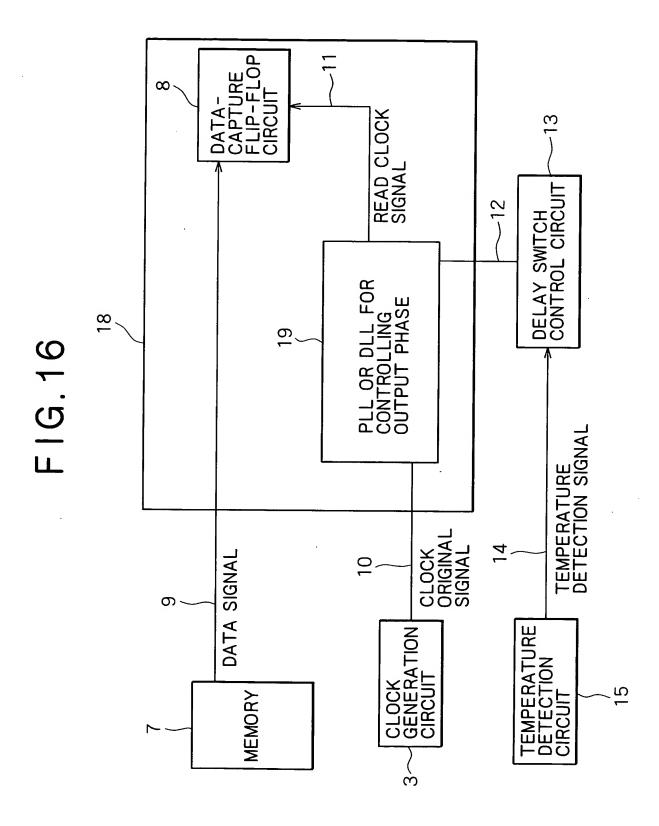




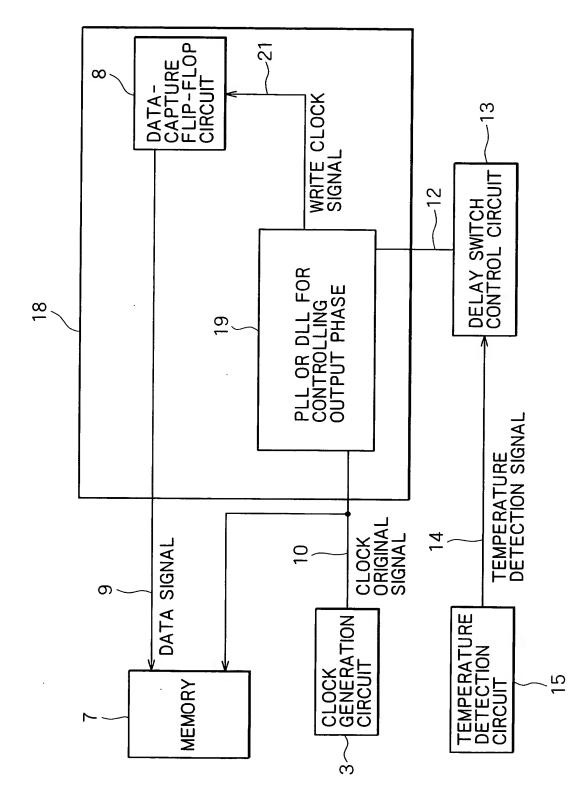


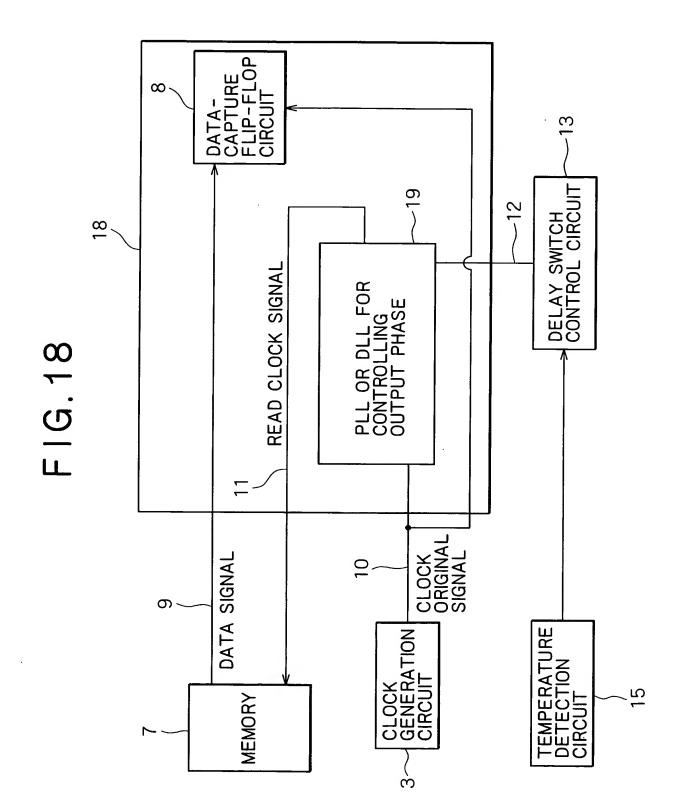






F1G.17





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